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Q. 5.19: A sequential circuit has three flip-flops A, B, C; one input x_{in} ; and one output y_{out} . ~~Q. 5.4: A PN flip-flop has four operations: clear to 0, no change, complement, and set to 1, when~~ ~~Q. 4.25: Construct a 5 to 32 line decoder with four 3 to 8 line decoders with enable and a 2 to 4~~ **Q. 3.15: Simplify the following Boolean function F, together with the don't-care conditions d, and** **Q. 3.9: Find all the prime implicants for the following Boolean functions, and determine which are [PDF]**

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6.28: Design a counter with the following repeated binary sequence 0, 1, 2, 4, 6 Use D flip-flops Q. 5.6: A sequential circuit with two D flip-flops A and B, two inputs, x and y; and one output z is ~~Getting Started in Digital Design~~ Graphic Design Books! | PaolaKassa

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Q. 2.15: Simplify the following Boolean functions T1 and T2 to a minimum number of literals. *Digital Design \u0026amp; Computer Architecture - Lecture 4: Combinational Logic I (ETH Zürich, Spring 2020) Q. 4.30: Using a decoder and external gates, design the combinational circui defined by the followin*

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